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## **Job Description – Test Engineer [SVH023] San Jose, CA**

The position of Test Engineer is responsible for driving and executing test and failure analysis projects out of our Silicon Valley Hub (SVH).

Starting with customer engagements, the role consists in planning projects; detailing the expected outcome (manage the expectation); developing a test solution (test program, loadboard, re-packaging if needed); running the tests and associated fault isolations. Presto Engineering has a specific emphasis on scan-test and BIST.

Candidates must be self-driven contributor, capable of multiplexing in a multi-disciplinary environment, and comfortable with direct customer interactions.

### **Responsibilities**

- Pre- and post-sales technical support, direct contact with customers
- Test solution development
  - Test program development (Inovys, Agilent, Advantest, Credence)
  - Loadboard specification and development
- Fault isolation projects in the lab (working with FA engineers)
- Written reporting

### **Requirements**

- MS EE or equivalent
- Minimum 5 years experience in ATE
- Direct semiconductor product engineering experience preferred
- Excellent communication skills

Flexible hours

Limited travel

### ABOUT PRESTO ENGINEERING, INC.

Presto Engineering provides comprehensive semiconductor test and analysis solutions to IDM and fabless companies, helping to improve the speed and predictability of new product releases. Presto combines unique technical expertise, extensive industry experience and state-of-the-art test, failure analysis and fault isolation capabilities to offer a complete product engineering solution designed to complement the internal product engineering resources of its customers. Presto Engineering is headquartered in San Jose, Calif. Additional information is available on the company's website at [www.presto-eng.com](http://www.presto-eng.com).